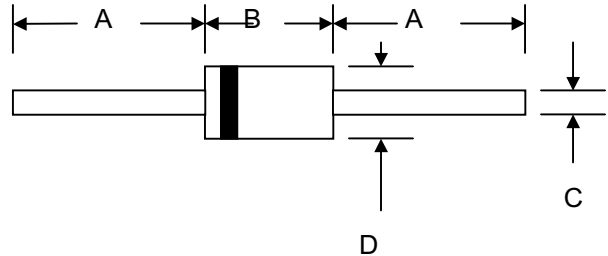


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**Features**

- Diffused Junction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability
- Green Products in Compliance with the RoHS Directive



**Mechanical Data**

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 1.2 grams (approx.)
- Mounting Position: Any
- Marking: Type Number
- Epoxy: UL 94V-O rate flame retardant

DO-201AD				
Dim	Min	Max	Min	Max
A	25.4	—	1.000	—
B	8.50	9.50	0.335	0.374
C	1.20	1.30	0.047	0.051
D	5.0	5.60	0.197	0.220
	In mm		In inch	

**Maximum Ratings and Electrical Characteristics** @T<sub>A</sub>=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

Characteristic	Symbol	SF61-G	SF62-G	SF63-G	SF64-G	SF65-G	SF66-G	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>							
Working Peak Reverse Voltage	V <sub>RWM</sub>	50	100	150	200	300	400	V
DC Blocking Voltage	V <sub>R</sub>							
RMS Reverse Voltage	V <sub>R(RMS)</sub>	35	70	105	140	210	280	V
Average Rectified Output Current (Note 1)	I <sub>O</sub>	6.0						A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I <sub>FSM</sub>	150						A
Forward Voltage @I <sub>F</sub> = 6.0A	V <sub>FM</sub>	0.975				1.3		V
Peak Reverse Current @T <sub>A</sub> = 25°C At Rated DC Blocking Voltage @T <sub>A</sub> = 100°C	I <sub>RM</sub>	5.0				100		μA
Reverse Recovery Time (Note 2)	t <sub>rr</sub>	35						nS
Typical Junction Capacitance (Note 3)	C <sub>j</sub>	120				60		pF
Operating Temperature Range	T <sub>j</sub>	-65 to +125						°C
Storage Temperature Range	T <sub>STG</sub>	-65 to +150						°C

**\*Glass passivated forms are available upon request**

- Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case  
2. Measured with I<sub>F</sub> = 0.5A, I<sub>R</sub> = 1.0A, I<sub>RR</sub> = 0.25A. See figure 1.  
3. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

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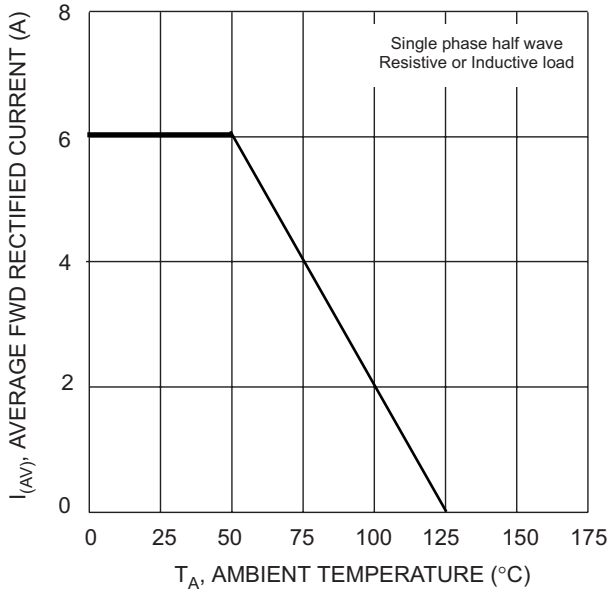


Fig. 1 Forward Current Derating Curve

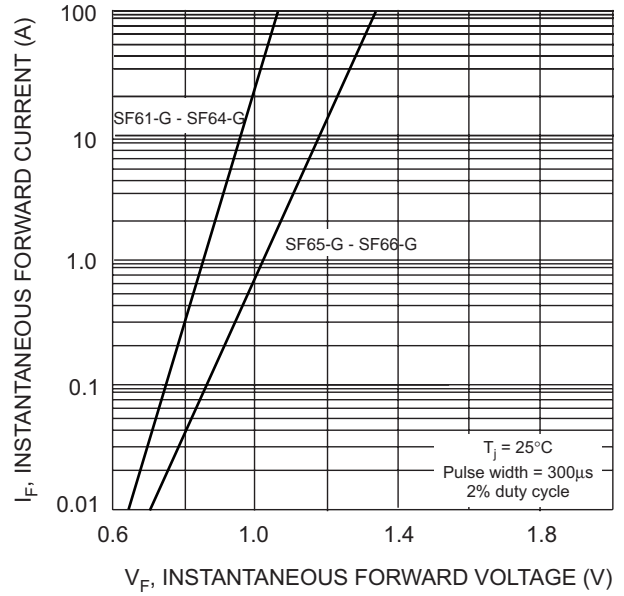


Fig. 2 Typical Forward Characteristics

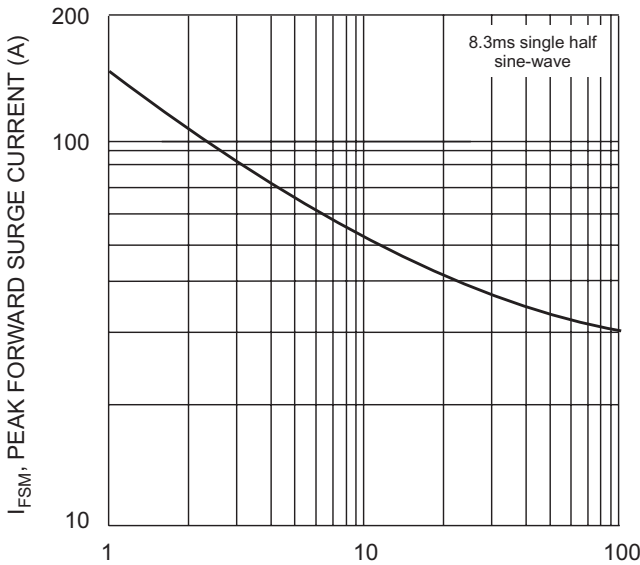


Fig. 3 Peak Forward Surge Current

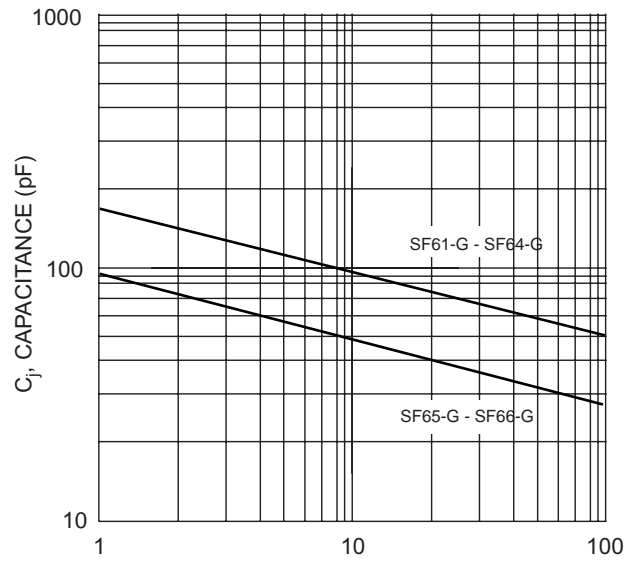
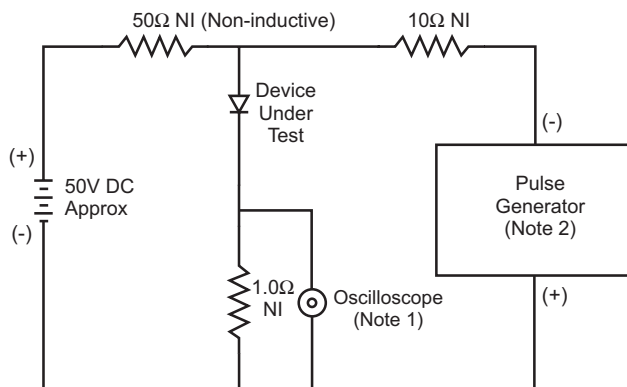
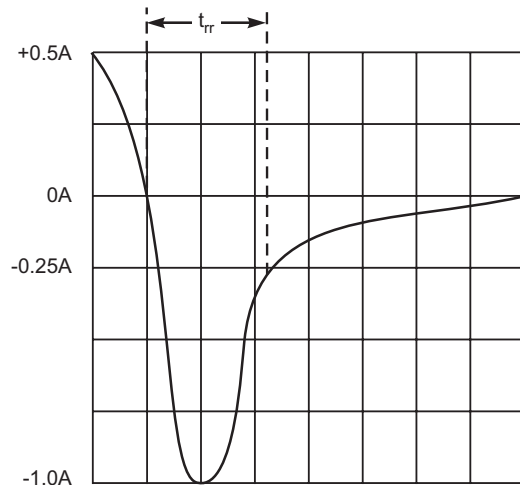


Fig. 4 Typical Junction Capacitance



- Notes:
1. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.
  2. Rise Time = 10ns max. Input Impedance = 50Ω.



Set time base for 5/10ns/cm

Fig. 5 Reverse Recovery Time Characteristic and Test Circuit

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